

FIG. 1 Cell Placement Region

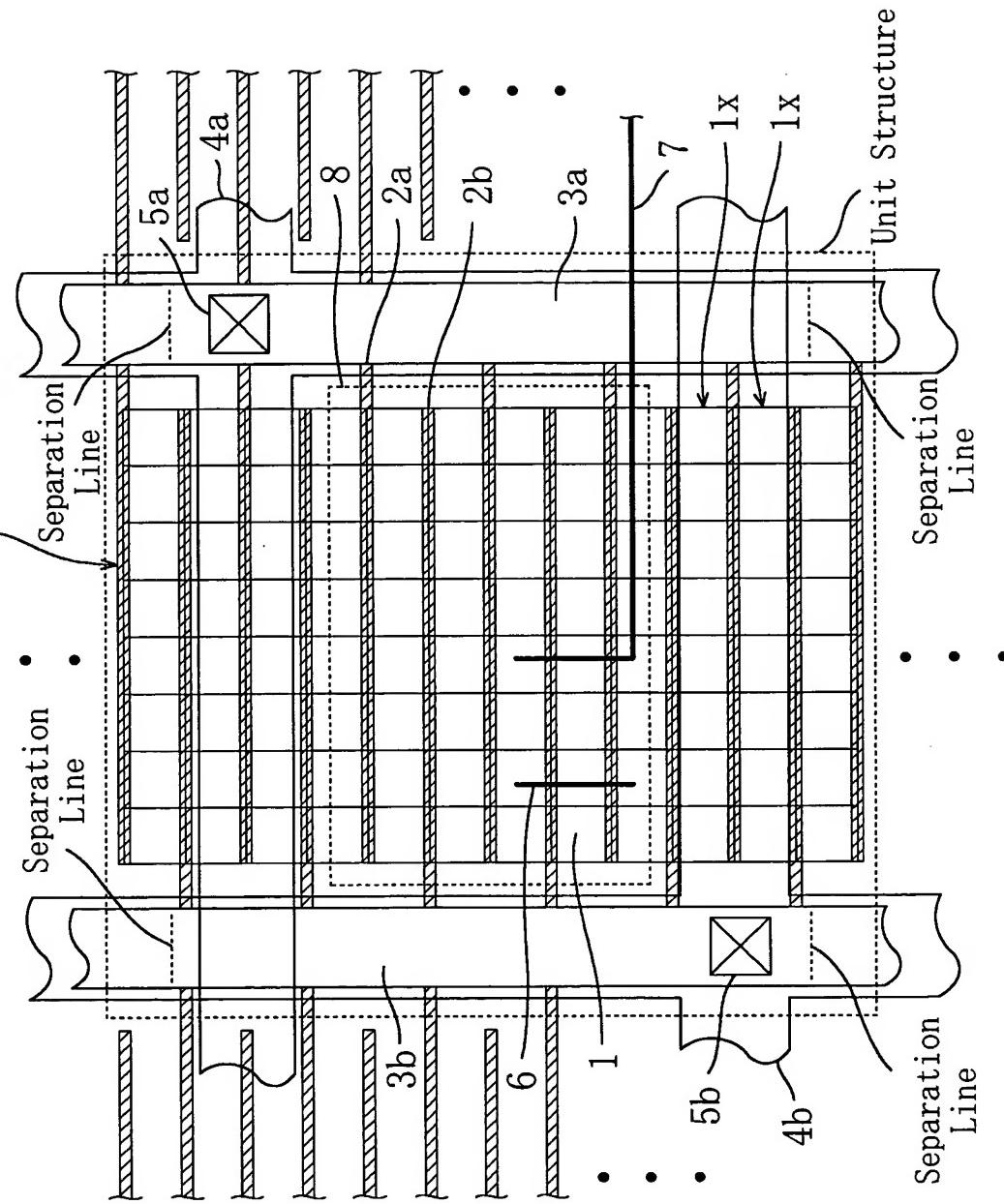


FIG. 2

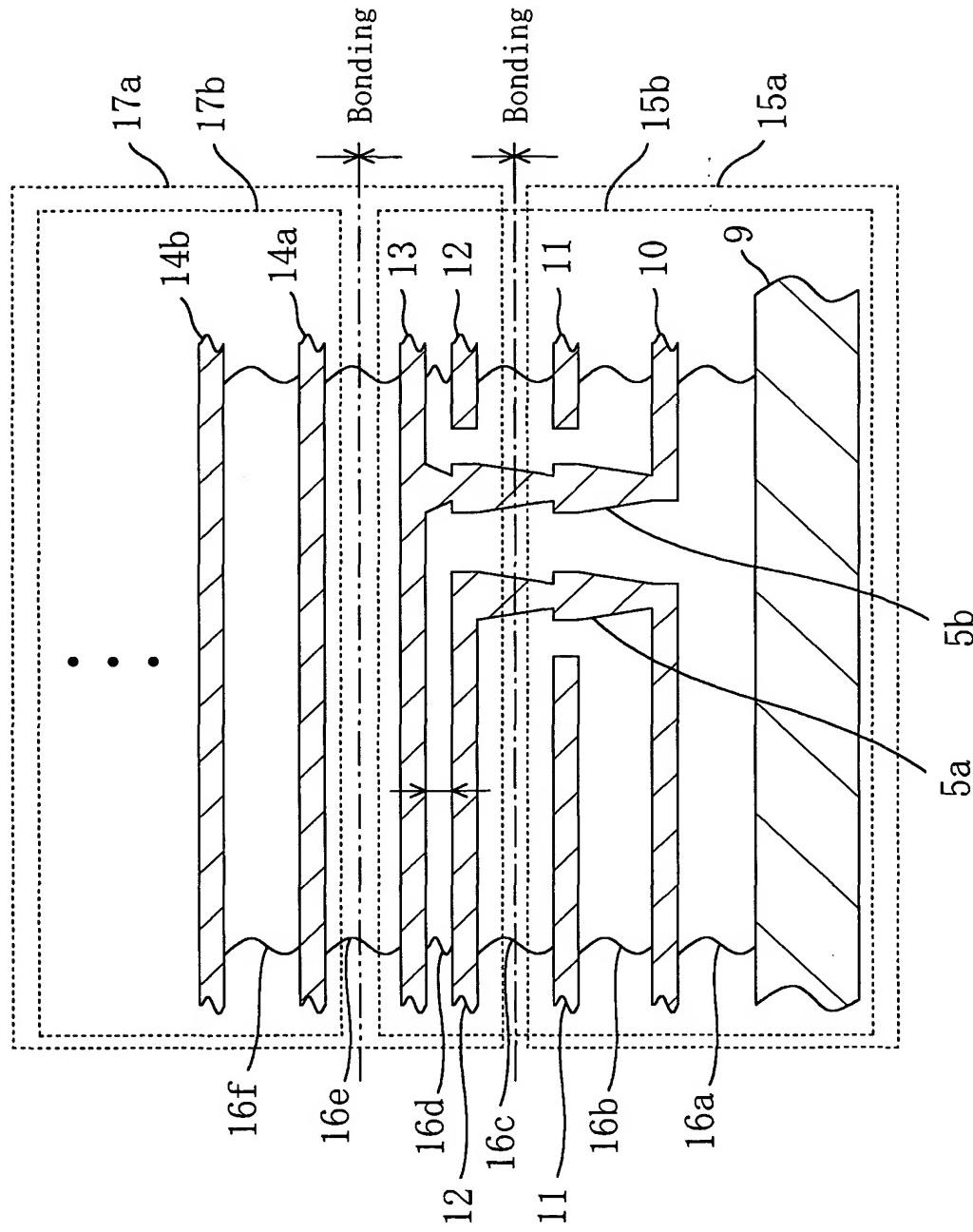
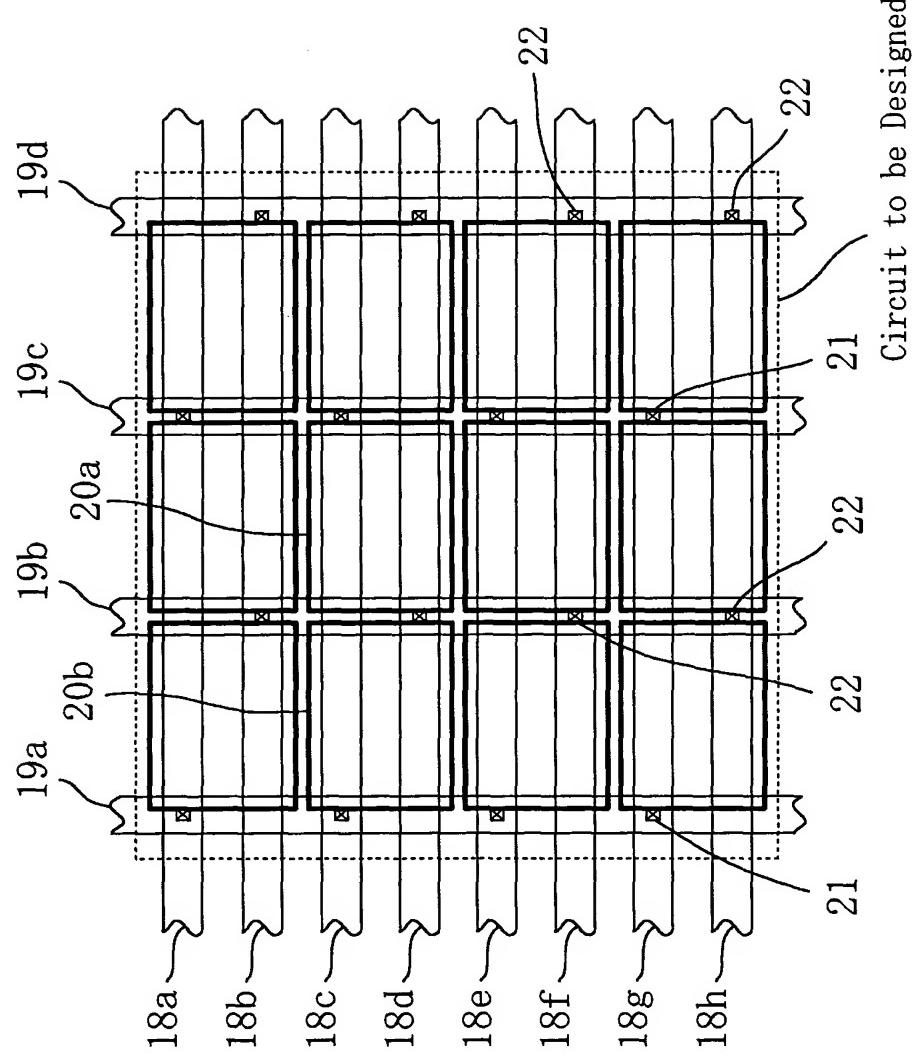


FIG. 3



450 T 0050 " 24547200 T

FIG. 4

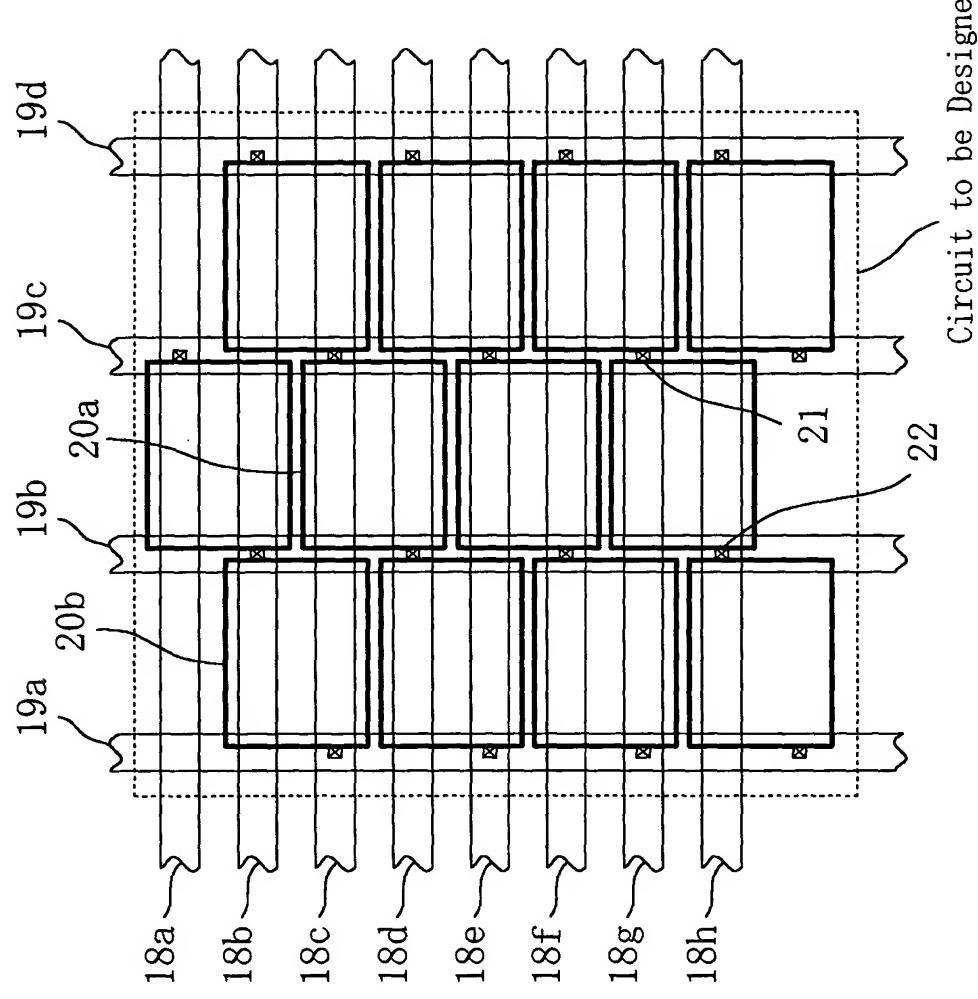


FIG. 5A

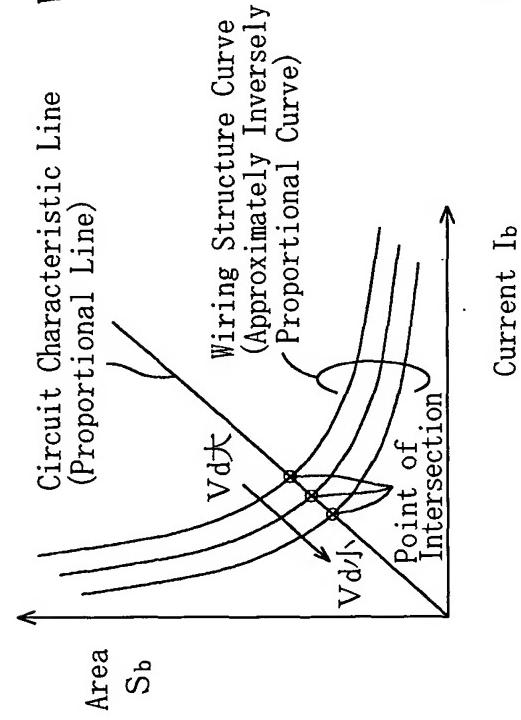


FIG. 5B

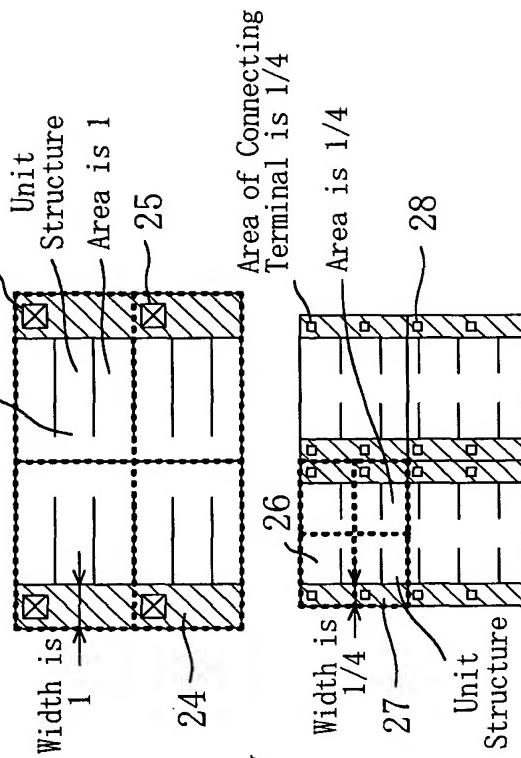


FIG. 5C

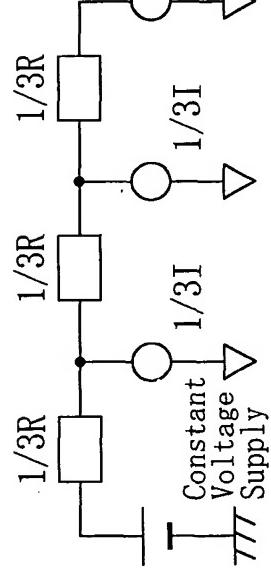


FIG. 5D

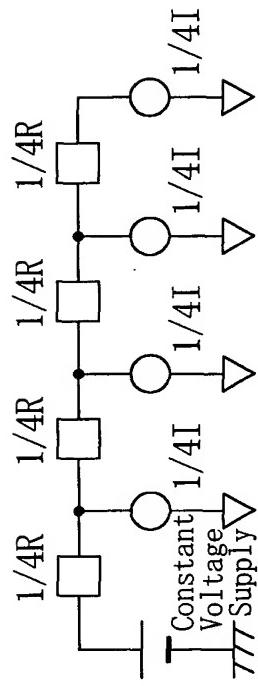


FIG. 6

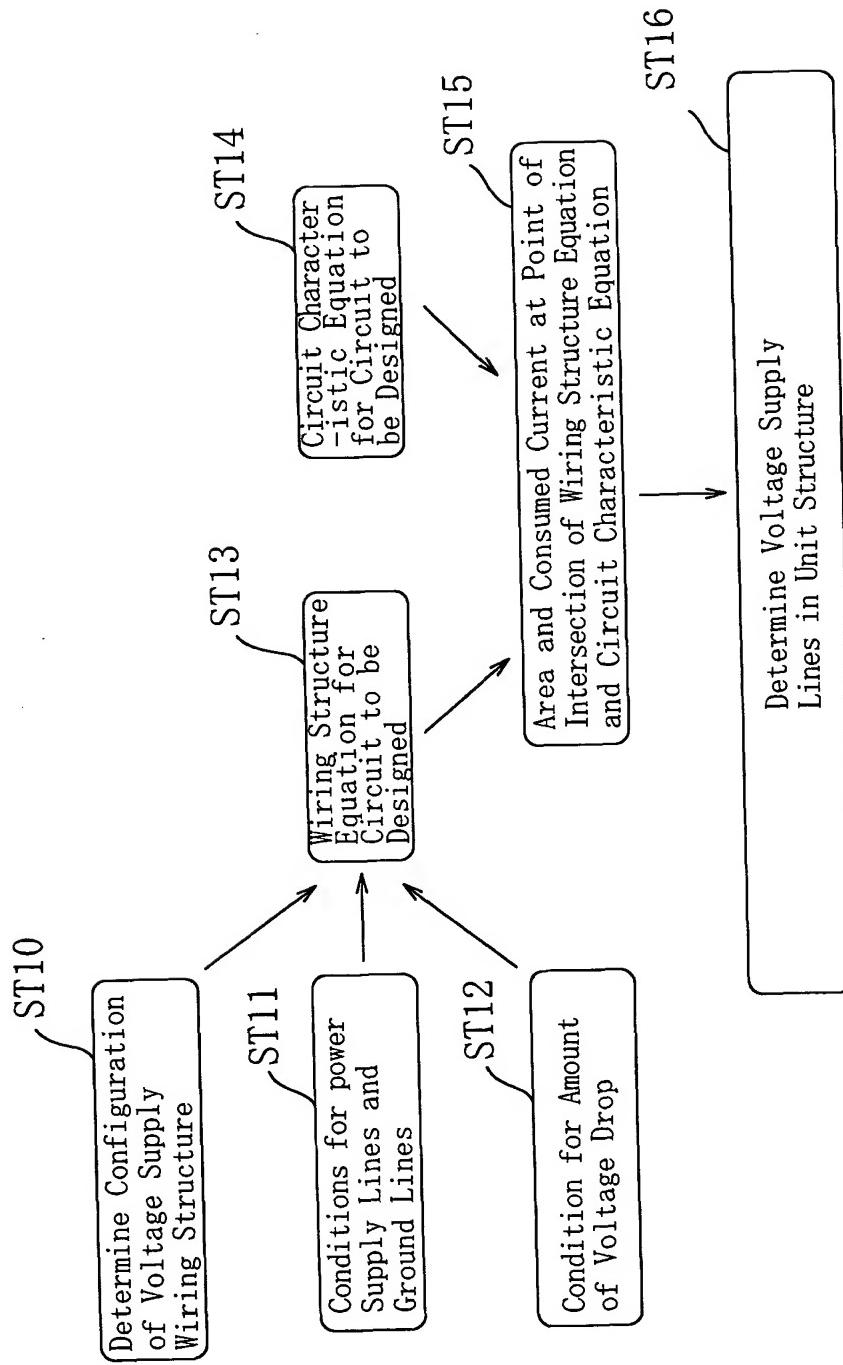


FIG. 7  
20-1  
20-2  
20-3  
20-a  
20-b  
40-a  
40-b  
41-a

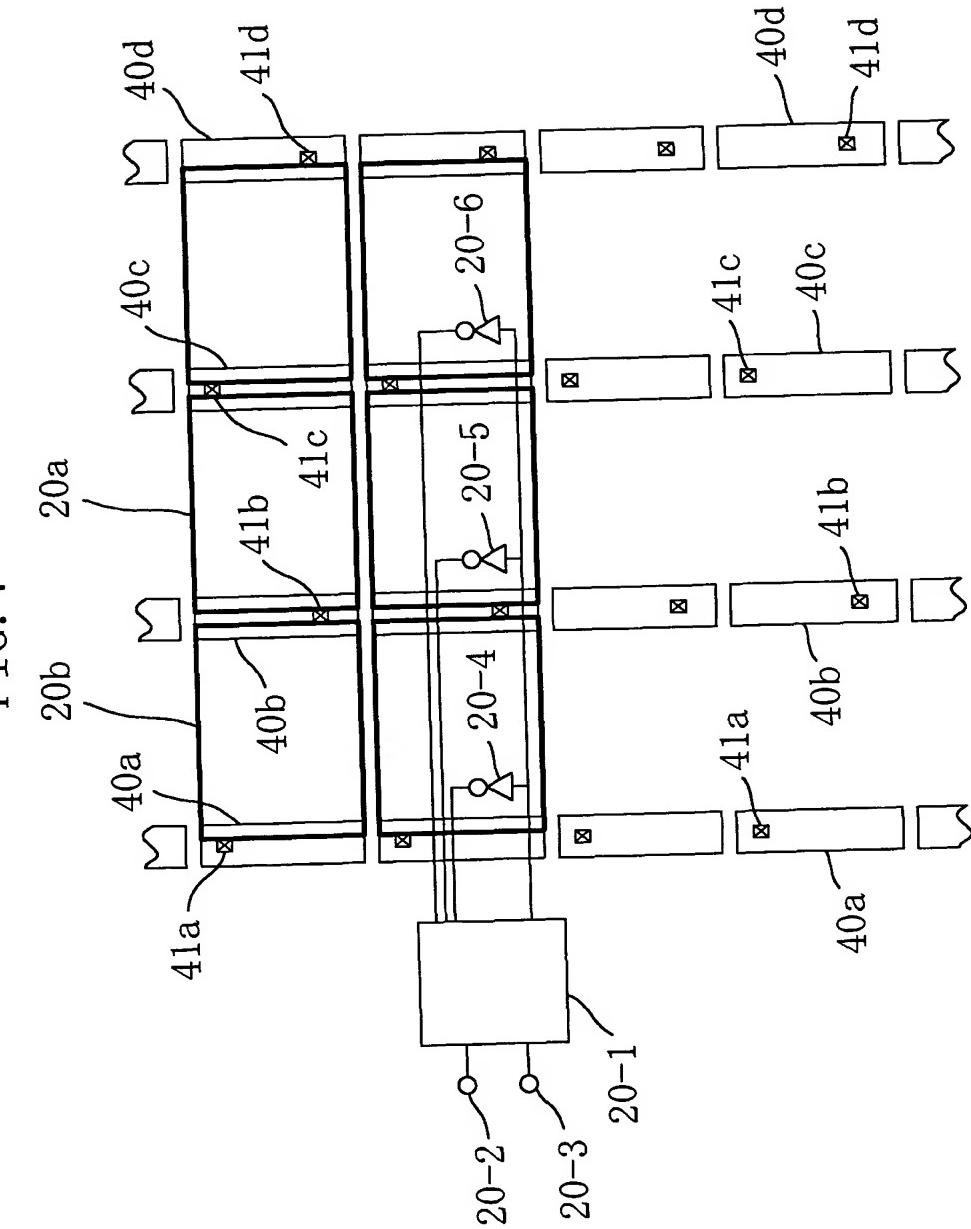


FIG. 8

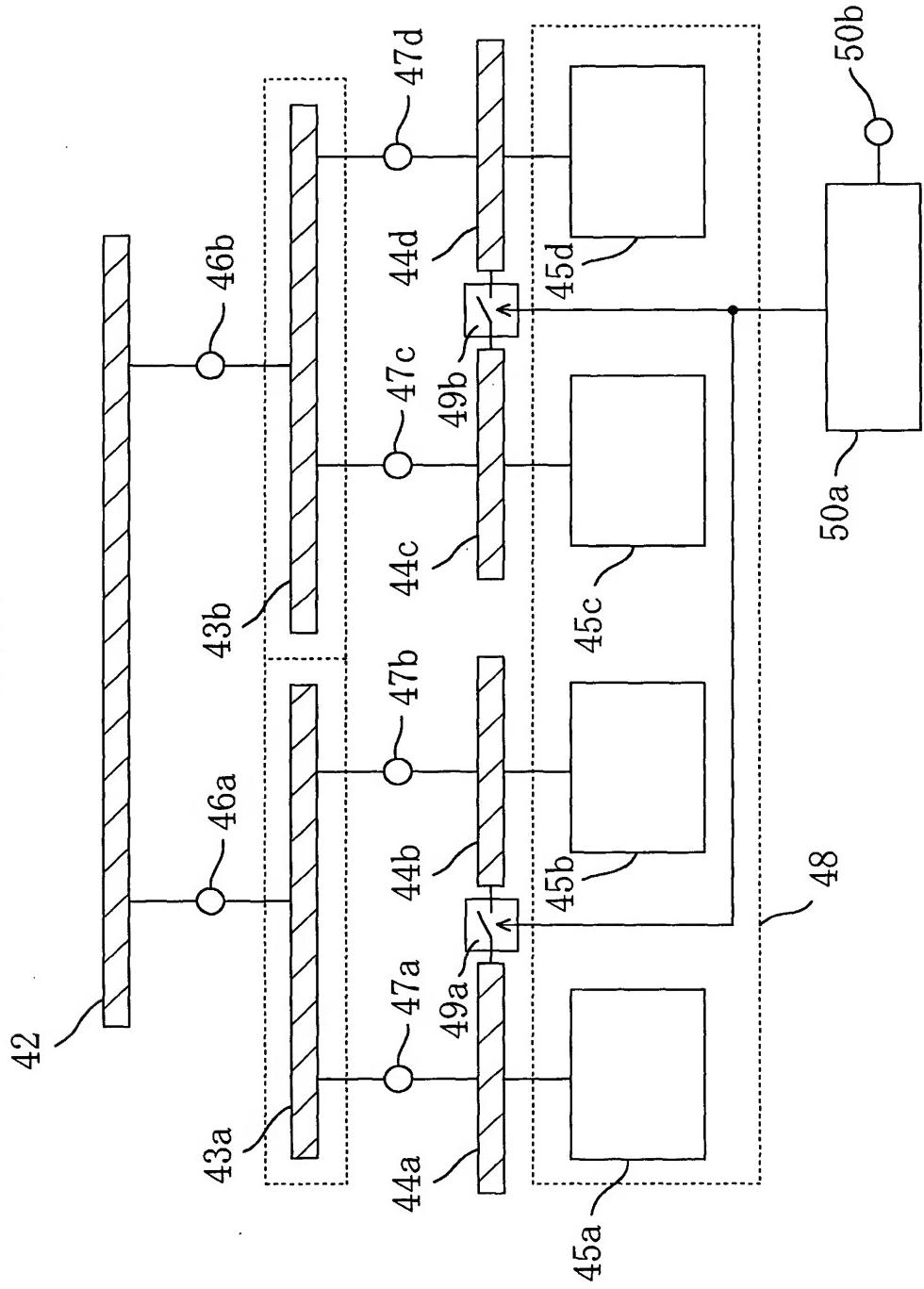


FIG. 9

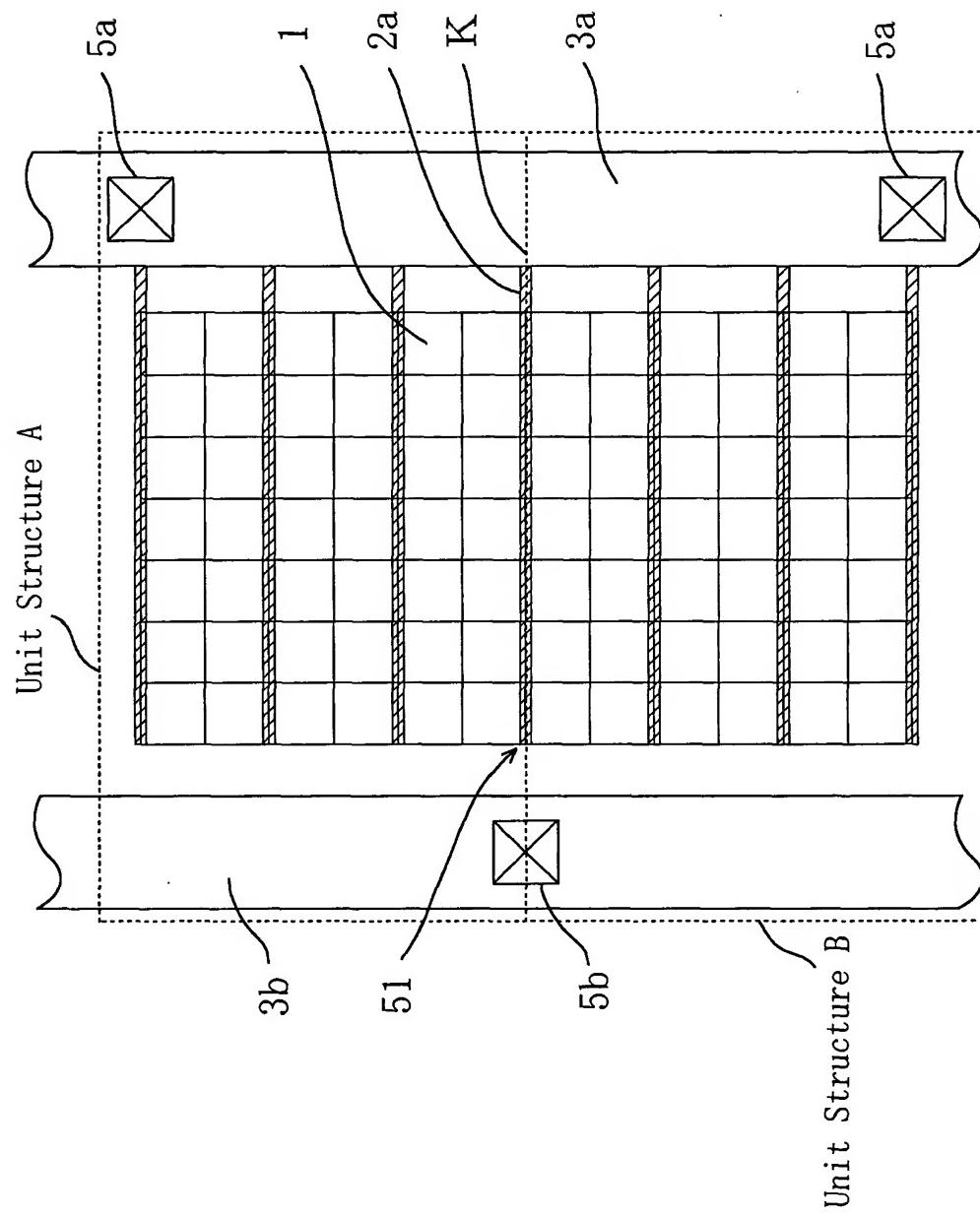


FIG. 10 PRIOR ART

Circuit to be Designed

